
Modelling and compilation method for multi-PLC control program

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Abstract: For the large-scale multi-PLC control system, a network-oriented programming method is proposed. Regarding the control network as a large-scale virtual PLC device, the engineers can program the whole system using all of the resources in the control network directly. A modelling and compilation method for control program is also put forwarded and an event graph model for the programs is created. The serial control program can be decomposed into parallel tasks by event graph model analyse and traversal rule. According to the topology of control network, the event graphs are grouped, and the instructions can be downloaded to the corresponding PLC automatically. The variables of devices can be synchronised through network communication. The experimental result shows that this method can improve the program efficiency of the networked PLC control system, and keep the running logic according with the original program.

Keywords: networked PLC control system; system modelling; program compilation; discrete event system; event graph.

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1 Introduction

As the development of computer, communication and network control technology, programmable logical controller (PLC) plays an increasingly important role in the field of industry control. The control mode is changing from traditional single PLC control/network communication toward large scale multi-PLC network control system. Since the low cost, easy maintenance and great flexibility, the networked control system becomes a hot topic. Yin et al. (2013) put forward a modelling and controller design method of networked control system over the integrated management platform system (IPMS). Zhu and Guo (2012) proposed an optimal controller to stabilise the closed-loop networked control system with access constraints and network-induced delays. Zhang et al. (2012) presented a quasi T-S fuzzy model which includes the performance of network with time-delay and packet dropouts. However in engineering applications, there are still many limitations and inconveniences in the programming operation of networked multi-PLC control system.

Usually the developers need to understand the topology of the control system and program for each controller respectively. It will become difficult when the number of controllers is large and the control logic is complex. For multi-PLC control system, the program should be developed in a network-oriented way. The control network should be regarded as a large virtual PLC and the developer can use all of the resources in the network directly. The program should be analysed and decoupled into parallel tasks by compiler, and downloaded into each corresponding device automatically. Among the recent PLC program compilation methods, Yan and Chen (2010) proposed a method to avoid the execution of partially useless instructions in PLC program. Tang et al. (2011) analysed the compiling of the PLC ladder diagram, and presented a node compilation method for soft PLC in the VC++ environment. Deveza and Martins (2009) proposed a translation methodology to emulate PLC control program in the MATLAB environment for program simulation. Chmiel et al. (2002) presented a program interpretation method of ladder diagram for a small compact type of PLC. Liu (2009b) used the state space compilation for program simplification. Xu (2009) used the linear matrix inequality compilation for hybrid variable separation. These methods realise the program compilation, optimisation and simulation from different aspects for a single controller. There is no sufficient method to realise the network-oriented programming for networked multi-PLC control system right now.

Discrete event system is a common tool of analysing the dependency relationship of events, which is widely used in various areas in industrial engineering (Luo et al., 2007). In the context of this paper, we convert the control system into

discrete event system, and analyse the dependencies of variables and instructions of the program, transit serial control programs into parallel discrete events, then assign the variables and instructions to different devices. Recently, discrete event system modelling tools include Automata, timed petri-net, event graph, etc. Since the difficulty of analysis is increasing with the complexity of the system, the automata and timed petri-net are difficult to apply in complicated control systems. Event graph, with simplified expression method and powerful modelling capacity, received a wide application in discrete event system (Xia et al., 2012) and becomes a hot topic right now. Nazari et al. (2012) proposed a determination method of component blocking and network blocking in Fully Connected event graphs. Declerck (2011) analysed external trajectories and token deaths in event graphs. Amari et al. (2012) gave a max-plus control design for temporal constraints meeting in timed event graphs.

The major contributions of this paper are as follows:

- a network-oriented programming method for networked multi-PLC control system is proposed
- a modelling and compilation method for network-oriented program based on event graph is put forwarded.

The rest of the paper is organised as follows. Section 2 presents the event graph modelling method for a control program. Section 3 describes how to distribute the program into different PLCs and realise variable and logic synchronisation. Section 4 presents the operating example of the modelling and compilation method. Section 5 gives a summary of the conclusions and future work.

2 Control program modelling

2.1 Network-oriented programming method

Network is the basis of device management and program development in a networked multi-PLC control system. For different kinds of devices, parameters, variables and registers can be acquired by the device description. Running status and parameter configuration can be achieved by listening, inquiring and request/response operation. The name and input/output parameters of instructions can be got from the instruction description, and the control program of the whole network can be developed in one editing environment.

As shown in Figure 1, via the network-object programming method, the developer can operate the resources in PLC1, PLC2 and PLC3 directly and define intermediate variables such as `_swt1`, `_swt2`, `_tmpOut` according to the requirement. There’s no need to program

for each controller separately, and device topology is no longer necessary. These works will be completed by the compiler. There are 4 major tasks in compilation method:

- create an event graph model for converting serial control programs to discrete event systems
- analyse the dependency relationships to identify the parallel task sequences of the modelling by an improved depth first search method
- group the event sequence and distribute the control program into corresponding PLCs
- insert communication instructions to assure the variable and logic synchronisation.

2.2 Discrete event system and control program

Discrete event system refers to a dynamic event-driven system with a hopping change of system state. In this kind of system, events trigger the transition of states at an instantaneous and discrete time point (Liu, 2009a). Control program describes resource allocation and executing logic of the controller, which consists of variables and instructions. Variables store information and instructions process information. The state of PLC control system only changes when instructions executing, and completes instantly at a time point. Thus a complete PLC program can be converted into a discrete event system (Chen et al., 2012). In control system, PLC is the basic physical object and it is the entity of the discrete event system; Variables and parameters of PLC are the attributes of an entity; the collection of all the PLCs' variables and parameters are the state of the control system; the execution of the instructions leads to the change of system state, thus it is the event of the system.

The event graph is a modelling method of graphical discrete event. It describes the dynamic characteristic through the logic of events and temporal relations (Savage et al., 2005). State changes are associated with the occurrence of a system event and are pictured as event

vertices. The edge between two event vertices represents the conditions under which one event might cause the occurrence of the other event as well as the time interval between the two events (Schruben L and Yucesan E, 1994).

Figure 2 is the example of event graph, if condition en_1 is true when event e_1 is triggered at clock c_1 , event e_3 will be triggered after t_1 delays. Similarly, if condition en_3 is true when event e_3 is triggered at clock c_3 , event e_5 will be triggered after t_3 delays. The advantage of event graph is that the scheduling relations of events can be expressed visually and it can support the scheduling analysis and optimisation of events based on graph theory (Liu, 2009a).

To facilitate the subsequent discussion, the event graph can be expressed by a two-tuple $G = (E, A)$. The $E = \{e_1, e_2, \dots, e_3, \dots\}$ represents the set of e nodes in the graph, e represents single event object and e_i represents the number i event. $A = \{a_1, a_2, \dots, a_i, \dots\}$ is the set of directed arc a . $a_i = f(e_n, e_m) = (c, en, t)$ is defined as a triple on the arc between e_n and e_m , in which en is the schedule condition, t is schedule delay, c is the event scheduling marking, which represents the effects of e_n and e_m at the number c time point in one circle of task execution process (Cordes et al., 2010).

In the PLC control system, control program constituted by the commands and variables. Controllers follow the order 'from left to right, top to bottom' to implement the cyclic scanning, and any execution of an instrument triggered the variable change event. Set $I = \{i_1, i_2, \dots, i_j, \dots\}$ as instruction set, $i_i = f(v_n, v_m) = (C, EN, T)$ represents a unique variable in control program. C represents the instruction execution sequence, EN represents the value of 'EN' pin parameter of an instruction, T represents instruction execution time. Every change of the variables triggered by instructions is an event. Picture a variable as an event node, and when the node is traversed, the variable change event happened. Set $V = \{v_1, v_2, \dots, v_i, \dots\}$ as the variable change events collection, and $P = (V, I)$ is an event graph which describes the interactions between variables and instructions in a control program.

Figure 1 Network-object programming method (see online version for colours)

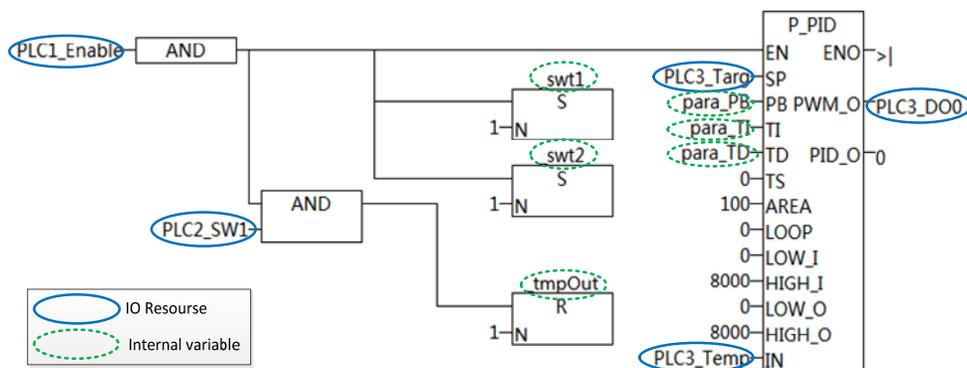
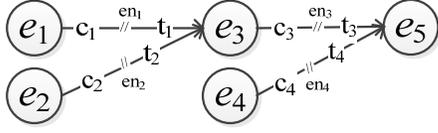


Figure 2 Example of event graph



2.3 Modelling the control program

Event graph modelling is the basis of analysis and compilation of PLC program. The model can be set gradually by analysis the variables and instructions and transforming the instruction into relationships between input and output parameters. In one PLC program, if the value of variable v_m must be determined before that of variable v_n , then v_m depends on v_n . Dependency relationship is the basis of analysis control program logic. If there are N input variables and M output variables, then there exists $N * M$ groups of dependency relationship. The dependency relationship of each group will correspond to one scheduling activity in event graph, which is a direct arc connecting two event nodes. The start node of an arc is the event occurred at the prior moment, and the closed node is the event occurred at the posterior moment. Then the instruction i_j can be expressed as:

$$i_j = (C_j, EN_j, T_j) = (c_1, en_1, t_1)(c_2, en_2, t_2) \dots (c_{m*n}, en_{m*n}, t_{m*n}) \quad (1)$$

Instruction collection I can be expressed as:

$$I = \begin{Bmatrix} i_1 \\ i_2 \\ \vdots \\ i_n \end{Bmatrix} = \begin{Bmatrix} (C_1, EN_1, T_1) \\ (C_2, EN_2, T_2) \\ \vdots \\ (C_n, EN_n, T_n) \end{Bmatrix} = \begin{Bmatrix} (c_{11}, en_{11}, t_{11}) & (c_{12}, en_{12}, t_{12}) & \dots & (c_{1a}, en_{1a}, t_{1a}) \\ (c_{21}, en_{21}, t_{21}) & (c_{22}, en_{22}, t_{22}) & \dots & (c_{2b}, en_{2b}, t_{2b}) \\ \vdots & \vdots & \ddots & \vdots \\ (c_{n1}, en_{n1}, t_{n1}) & (c_{n2}, en_{n2}, t_{n2}) & \dots & (c_{nk}, en_{nk}, t_{nk}) \end{Bmatrix} \quad (2)$$

Each row vector in equation (2) represents a set of schedule arcs of an instruction. The number of the scheduling arc

corresponding to each instruction is different. So there are different count of members in each row, namely, the value of a , b and k is independent and it only relates to their own corresponding instructions. The instruction set and variable change events set is two basic elements of the modelling. The main steps of modelling are as follows:

- use template variables to substitute logical connections, and convert the control program to instruction sequence
- map variables to the event vertices
- converting each instruction into sub event graphs including vertices and arcs
- connect each sub graphs with the same event vertices.

In the program shown in Figure 1, there are 11 variables, six instructions and two logical connections. It needs two pairs of template variables to substitute logical connections. Mapping relationships between events and variables are shown in Table 1.

Table 1 Mapping relationships between event nodes and variables

Event node	Variable	Remark
v11	PLC1_Enable	IO resource (PLC1)
v12	TMP1	Temporary variable
v21	PLC3_Targ	IO resource (PLC3)
v22	para_PB	Internal variable
v23	para_TI	Internal variable
v24	para_TD	Internal variable
v25	PLC3_Temp	IO resource (PLC3)
v26	PLC3_DO0	IO resource (PLC3)
v31	_swt1	Internal variable
v41	_swt2	Internal variable
v51	PLC2_SW1	IO resource (PLC2)
v52	TMP2	Temporary variable
v61	_tempOut	Internal variable

The instruction sequence is shown in Figure 3.

The numbers in the figure are the instruction ID ordered by execution sequence. Sub event graphs corresponding to each instruction are shown in Table 2.

Figure 3 Command sequence

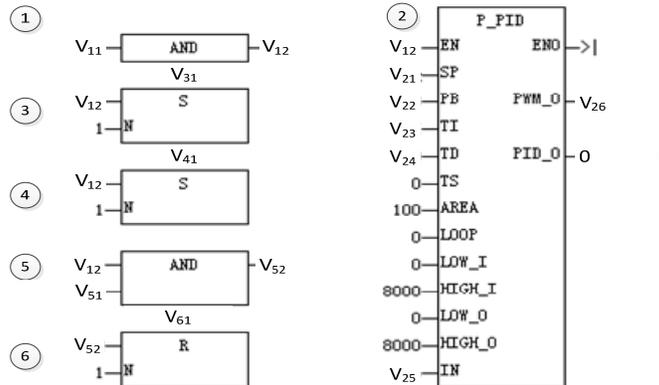
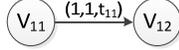
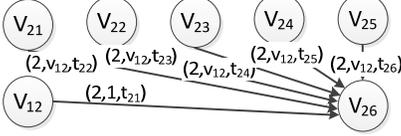
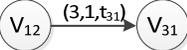
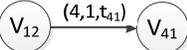
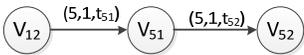
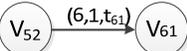
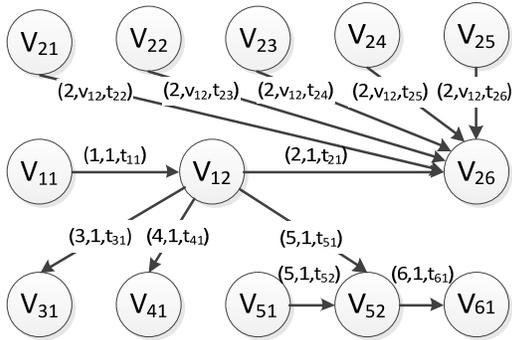


Table 2 Command and event graph

ID	Schedule arcs	Event graph
1	(1, 1, t_{11})	
2	(2, 1, t_{21}) (2, v_{12} , t_{22}) (2, v_{12} , t_{23}) (2, v_{12} , t_{24}) (2, v_{12} , t_{25}) (2, v_{12} , t_{26})	
3	(3, 1, t_{31})	
4	(4, 1, t_{41})	
5	(5, 1, t_{51}) (5, 1, t_{52})	
6	(6, 1, t_{61})	

Event graph model is built by connecting each sub graphs, shown in Figure 4.

Figure 4 Example of constructing event graph

3 Modelling analysis and program downloading

The event graph model describes the dependency relationship of the variables in PLC program. The serial control program can be decomposed into parallel tasks by modelling analyse and traversal. According to the topology of control network, the event graphs are grouped, and the instructions can be downloaded to the most appropriate PLC automatically. The variables of devices can be synchronised through network communication.

3.1 Traversal algorithm of event graph

An event graph traversal rule should be put forward in order to analyse the dependency relationship of the variables in PLC program. Based on improved Depth First Search(Cordes et al., 2010), the event graph can be

disintegrated into several event sequences, so that PLC program can be decoupled. These event sequences are called event tree. One event tree includes the information of events and scheduling activities. It describes that how a variable influences the other variables by specific instructions. Construction of the event trees is the intermediate process which PLC program should be disintegrated into parallel discrete events.

Depth first search is similar to the first traversal of a tree structure. It traverses from the vertex to each possible branch until every vertex is visited. Recursion includes following operations:

- visit the neighbour which has not been accessed
- set the *visited* property of the node to be *True*
- if the node has neighbours which have not been accessed, visit one of the neighbours in the same way, otherwise go back to the former node and continue searching until all vertices are visited.

In depth first search algorithm, the access order of nodes and arcs is random. However in PLC control program, instructions are always executed in the order from left to right, top to bottom. In event graph, parameter c in equation (2) represents the execute sequence of the correspond instrument. The arcs with smaller c value should be scheduled preferentially. Therefore the access order of neighbours in recursion operation c should be restricted. According to the in-degree and out-degree of the event nodes, which are classified into eight types: zero-input single output, zero-input multi-output, single-input single-output, single input multi-output, multi-input single-output, multi-input multi-output, single-input zero-output, multi-input zero-output. The improved DFS

algorithm has different traversal rules to process these different kinds of nodes. To facilitate discussion, set scheduling marking parameter c to be the weight of arcs in event graph.

Table 3 shows the method of path selection in recursion operation. Once traverse, a new node, the current node and the arc connection between them should be inserted into event tree. Among traversal rules in Table 3, the process of multi-input single-output node is relatively complex. It describes the situation that the value of an instruction's input parameter is modified by many other instructions in the program. The processing method depends on the execution order of these instructions. The pseudo code of the process method is shown in Figure 5. It is also the foundation of process of multi-input multi-output and multi-input zero-output nodes.

Table 3 Improved DFS traversal rules

Node type	Traversal rule
Zero-input single output	Vertex, visit the next adjacent node.
Zero-input multi-output	Vertex, visit the adjacent node Corresponding to the smallest weight arc.
Single-input single-output	Visit the next adjacent node.
Single-input multi-output	Visit the adjacent node corresponding to the smallest weight arc.
Multi-input single-output	Choose the traversal path according to the weight of input and output arcs, algorithm is shown in Figure 5.
Multi-input multi-output	Randomly select a certain output arc in turn. Consider the present node as multi-input single-output node. After that select the next input arc, repeating the above operation.
Single-input zero-output	Add the present node into event tree and Stop traverse this path.
Multi-input zero-output	Consider the present node as multi-input single-output node, and the weight of output arc is infinity.

Figure 5 Process of multi-input single-output node

```

IF (OUT < INnow) {
  FOREACH (INx) { IF (INx < OUT || INx > INnow) END; }
}
ELSE {
  FOREACH (INx) { IF (INnow < INx < OUT) END; }
}
ADDTOEVENTTREE (CURRENTNODE);

```

The weight of the unique output arc is out , and in_{now} represents the weight of the arc which is accessed by current traverse path. in_x is the weight of any other input arcs of the node. The pseudo code shows that if the weight of current arc in_{now} is less than output arc out , the node can be inserted into event tree only if there is no arc satisfied the condition of $in_{now} < in_x < out$. That means if a variable is used once

and it is modified by many instructions before use, only the last modified instruction is effective. Otherwise if the weight of current arc in_{now} is greater than output arc out , the node can be inserted into event tree only if there is no arc satisfied the condition of $in_x < out$ or $in_x > in_{now}$. That means if the value is modified only after used, the last modified instruction is effective.

3.2 Program distributed downloading

The input and output variables of the control system may locate at different devices. Due to the low transmission efficiency of industrial field bus, the bottleneck of large-scale data processing lies in the network bandwidth rather than device performance. Through optimising the parallel event sequence, get the calculation closer to the IO source. The network bandwidth can be saved and achieve high-efficiency computing performance. Industry evaluates it as "moving computing is more economical than moving data" (Hu and Feng, 2010).

Event Tree describes the interaction relationships of variables. One instruction may exist in many event trees, and different variables may influence a same variable by one instruction. Thus, event trees need to be grouped to guarantee variables in one Event Tree cannot be affected by other trees. The optimising process is as follow:

- at the beginning, divide each event tree into an independent group
- inspect whether the two event trees contain the same weights of arcs
- if containing the arcs with same weight and the destination nodes of the arcs are the same, two trees can be combined into one event tree
- repeat the second step until any two event trees do not fulfil the condition of the third step.

Thus, an event diagram shown in Figure 4 can be divided into the following four groups:

There is no coupling relationship between events and schedule activities in grouped events trees, and these trees can be assigned to different devices. The compiler must guarantee the regular communications and control logic, and also the overloading of single Device should be avoided. Therefore, there are two fundamental modes: Synchronised optimisation, decrease communication delay between devices and keep the synchronisation of control logic as much as possible; load balance, calculate and compare the expected scheduling time of each assignment scheme, get the shortest one to reduce the execution cycle of single device (Yong et al., 2010).

After the optimisation, each group of event trees which is independent from each other can be distributed to different devices. One event tree can be downloaded into different devices. If there exists a set of N groups of event trees = $\{T_1, T_2, \dots, T_N\}$, T_i contains the set of variables $V_i = \{v_{i1}, v_{i2}, \dots, v_{ik}\}$, $1 \leq i \leq N$, The basis of the optimisation of device assignment is the position of physical

IO. If there exists a set of devices $D = \{d_1, d_2, \dots, d_M\}$, d_i is the device containing practical IO, d_i contains the variable set $VD_i = \{v_{i1}, v_{i2}, \dots, v_{ik}\}$, the elements in VD_i is the physical IO of the device and $1 \leq i \leq M$. If \emptyset_{ab} is the time of transmitting one variable between d_a and d_b , the measurement unit is (ms), ρ_{i1a} is the number of elements contained in $V_i \cap VD_a$, $W_i = \{w_{i1}, w_{i2}, \dots, w_{iM}\}$ is the communication time which is produced after l_i is allocated to each device. The communication time which l_i is allocated to device d_a is shown by equation (3).

$$w_{ia} = \rho_{i1} \times \emptyset_{a1} + \rho_{i2} \times \emptyset_{a2} + \dots + \rho_{iM} \times \emptyset_{aM} \quad (3)$$

T_i should be assigned to the device which produces the minimum communication time. If $w_{ia} = w_{ib}$, then assigned T_i to the device who has less instructions.

Set the event tree shown in Figure 6 as an example. There exists device set $D = \{d_A, d_B, d_C\}$ and event tree set $T = \{T_1, T_2, T_3, T_4\}$. If $\emptyset_{AB} = 50$, $\emptyset_{BC} = 20$, $\emptyset_{AC} = 80$, in event tree T_1 , $w_{1a} = 240$, $w_{1b} = 290$, $w_{1c} = 50$, and w_{1c} is the minimum, so allocate a to device d_c . In the similar way, communication time of other events trees and devices is shown in Table 4. In event tree T_4 , both w_{4b} and w_{4c} are the minimums. Since the number of instructions in device d_b is smaller, T_4 is assigned to device d_b .

Table 4 Communication delay list

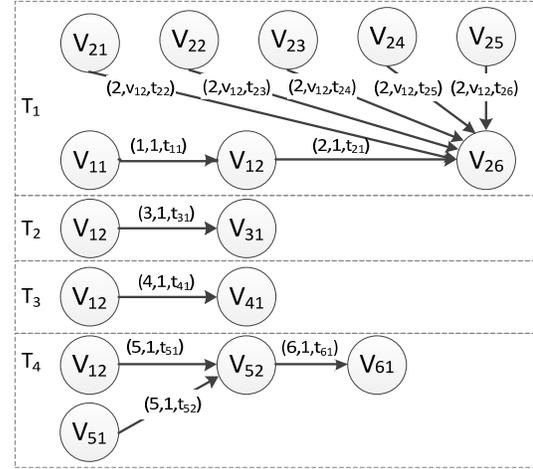
Event tree i	w_{ia} (ms)	w_{ib} (ms)	w_{ic} (ms)	Target device
1	240	290	50	d_c
2	80	20	0	d_c
3	80	20	0	d_c
4	130	20	20	d_b

3.3 Variable synchronisation

Different event trees may contain same event. After assigning devices for event tree, the same variable may exist in different devices. In the example shown in Figure 6, variable v_{12} will be assigned to device d_a , d_b and d_c . In order to guarantee the correct logic of the program, devices should read the real time data from other devices and synchronise to the local corresponding variable (Liao and Hu, 2009). The basic principles of variable synchronisation are as follow:

- if a variable is allocated to two or more devices, it will be duplicated into many replicas and executed in each device
- allocate buffers for each device and store the value of the replicas of which are read from different devices
- insert communication instructions, read the value of from other devices into local input buffer or write the value of into output buffer of other devices.

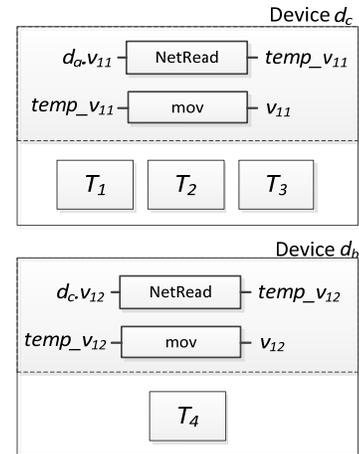
Figure 6 Event graph grouping



Usually input parameters of instructions need to be synchronised. Communication instruction should be inserted at the beginning of the program. Read the value of variables into local input buffer and use replicas to replace the variable. Sometimes output parameters need to be written to the physical output interface of other devices. Communication instruction should be inserted into the end of program and write the value of parameter into the output buffer of the other device. Insert instructions into the other device to read the data from the output buffer, so that the output variable synchronisation will be realised.

Figure 7 shows the variable synchronisation operation of events trees in Fig.6. Event tree T_1 is assigned to device d_c , so instructions need to be inserted into the beginning of program in d_c to read the value of variable v_{11} in d_a . The value should be stored into buffer and replace the variable v_{11} in event tree T_1 . Similarly Event tree T_4 is assigned to device d_b , instructions need to be inserted into the beginning of program in d_b to read the value of variable v_{12} in d_c . The value should be stored into buffer and replace the variable v_{12} in event tree T_4 .

Figure 7 Synchronisation demonstration



4 Operating examples and analysis

The modelling and compilation method for multi-PLC control program proposed by this paper has already been applied and verified in the central control system of a compressor station in a shipbuilding industry group. The networked multi-PLC control system monitors and controls eight groups of compressors, dryers and air filters, and six groups of pumps and cooling towers. Also the system monitors the total tank pressure and air consumption of the factory. Topological structure of system is shown in Figure 8.

The engineer station executes programming software. Engineers programmed the whole system using the network-oriented program and compilation method proposed in this paper, and replaced the original program; The monitor and control centre monitoring manual operation signal and displaying human machine interface; Networked control system is made up by 20 PLCs. It monitors the speed, Shaft vibration and movement, surge, and also the pressure and temperature of compressors; and controls the temperature and humidity of dryers, speed and flow velocity of pumps and the pressure and temperature of inflow and outflow of the cooling towers.

We have upgraded the program software and used the program developed by new method proposed in this paper to replace the original one. During daily operation, the control logic was consisted with the original program. But the efficiency of development was greatly improved since the engineer could regard the whole network as a large virtual PLC without concerning the topology of the controllers any more. The modelling and compilation method is demonstrated by using the example of dryer temperature control program, shown in Figure 9. Here is the explanation for the devices and variables in this figure.

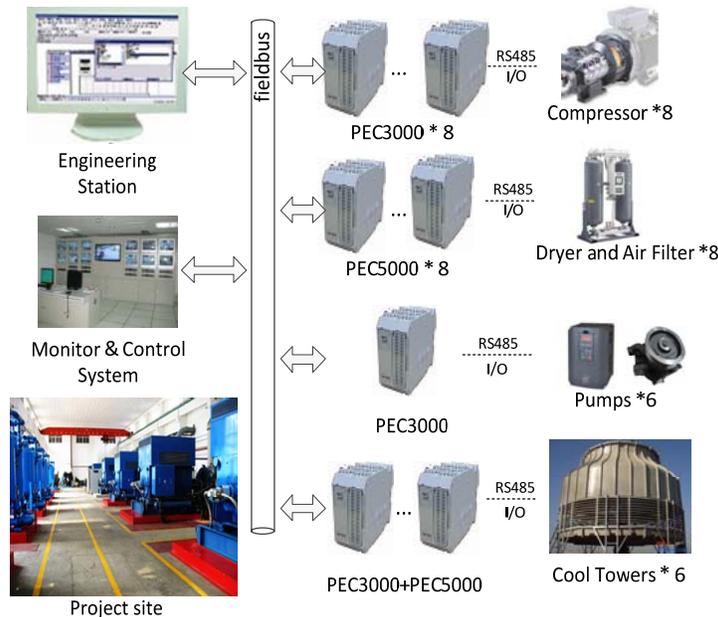
Device *ConDev* : located in monitoring centre, collect the manual operation; *ConDev_Dry1*: the start and stop control variable of 1# dryer; Device *DryDev1* : the temperature control device of 1# dryer; *DryDev1_Temp*: the temperature as the PID instruction's feedback input parameter; *DryDev1_DOO*: the first digital output signal of device *DryDev1*, controls the heating of 1# dryer; *TargVal*, *para_PB*, *para_TI* and *para_TD* are internal variables, record the target temperature and *PB*, *TI*, *TD* parameters set by customers. Temperature control program for 1# and 2# dryers is shown in Figure 9 and the other dryers' program are similar to it.

According to the modelling building method in Section 2, an event graph model can be built for the program, as shown in Figure 10. The graph consists 14 nodes and 17 arcs, in which, *tmp1~tmp3* are automatically inserted temporary variables which replace the logical connections. Other mapping relationships between nodes and variables are list in the right part of the figure.

By using traversal rules and event tree grouping methods in Section 3, the event graph in Figure 10 can generate two event trees with no coupling relationships. The event trees will be download separately to device *DryDev1* and *DryDev2*, shown in Figure 11.

The control program is compiled and downloaded to different controllers and its logic is parallel running. The control logic is consisted with the original program. To evaluate the modelling a compilation method, we compare the program distributed into each sub system with the original one, and count the number of instructions and communication instructions inserted into the program by the compiler.

Figure 8 System topological structure (see online version for colours)



Project site

Figure 9 Temperature control program for 1#and 2# dryers

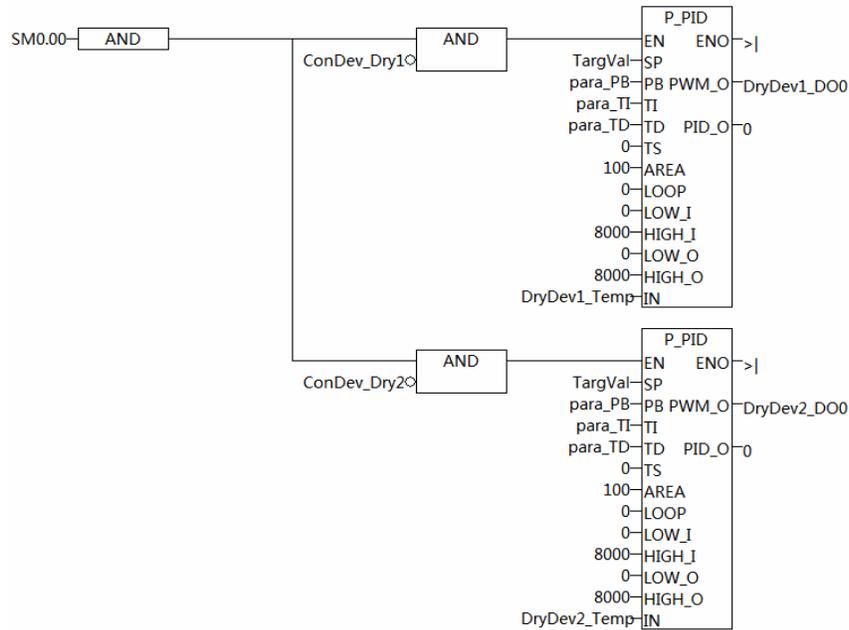


Figure 10 Discrete event model

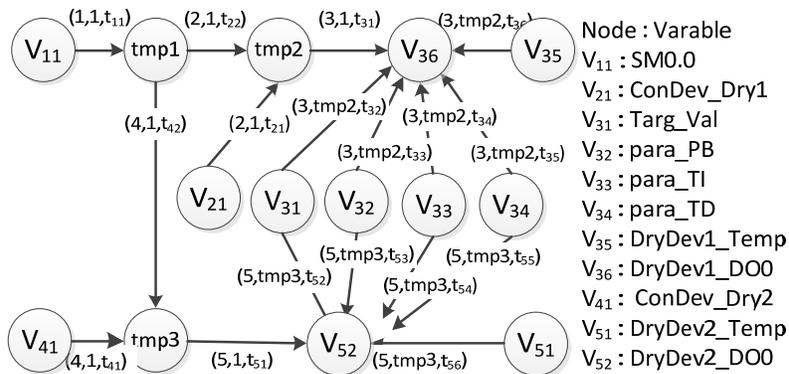


Figure 11 Optimised grouped event trees

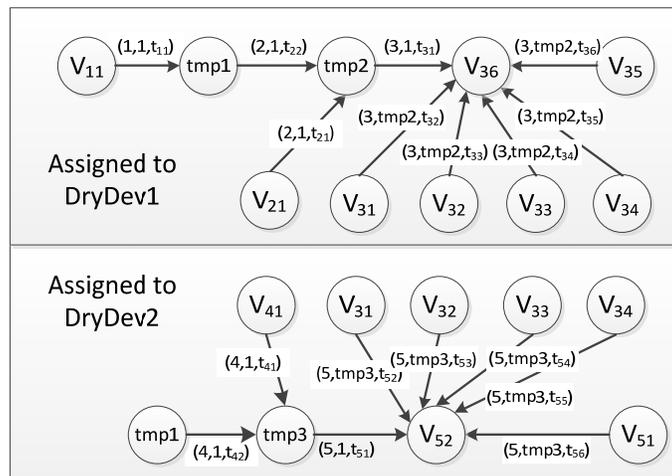


Table 5 Dispersed effect of downloading control programs

<i>Sub system</i>	<i>Instructions count</i>	<i>Added instructions</i>	<i>Original program</i>
Network-oriented program	1,776	NA	NA
Compressor sys	696	16	649
Dryer and filter sys	584	40	552
Pumps control sys	180	12	166
Cooling tower sys	348	30	261
Monitoring centre	66	0	80

The control program is downloaded to five sub control systems. As other systems need to read the control command from the monitoring centre, network communication instructions are added by the compiling algorithm. As the existence of numerous monitoring parameters, program needs many serial communication instructions to read and write these parameters from the compressor controllers. Consequently, the number of instructions is relatively big. At the meantime, we can also observe a big amount of communication instructions inserted by the compiler because the dryers and air filters and cooling tower control system need to read parameters from monitoring centre. The data in Table 5 illustrates that the number of instructions of programs downloaded in each sub system is similar to the original programs'. Without considering the developers' habit, the compilation algorithm generated a group of communication instructions for each shared variable. However, the developer allocated the shared variables at a consecutive address in original program then synchronised them together at the beginning and the end of the program. As a result, the number of communicated instructions in original program is smaller than the ones inserted by the compiler which is the point need to be optimised in this compilation algorithm.

5 Conclusions

Considering the large-scale multi-PLC control system, a network-oriented programming method is proposed. Regarding the control network as a large-scale virtual PLC device, the engineers can program the whole system using all of the resources in the control network directly. It prevents the inconvenient artificial operations of current method such as device topology analysing, individually programming, and variable and logic synchronisation. Consequently, it reduces the difficulty of system development and improves the programming efficiency. On this basis, an event graph model for the programs is created. The serial control program can be decoupled into parallel tasks by the analyse and traversal rule of the event graph. And the program can be dispersed downloaded into different devices by method of event tree grouping and variable synchronisation. The method has been applied and verified in a local shipbuilding industry group. The application result shows that the compile method can

improve the program efficiency of the networked PLC control system and satisfied the application requirement. It has a high application value of the program design and compilation in multi-PLC control system.

The limitation is that the expected execution time needs to be calculated and compared for all of the devices and the event trees at the device assignment operation. It will reduce the compilation efficiency when there is a large number of event trees and devices. And the variable synchronisation method needs to be optimised as shown in the last part of Section 4. Further research is needed to address these points.

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